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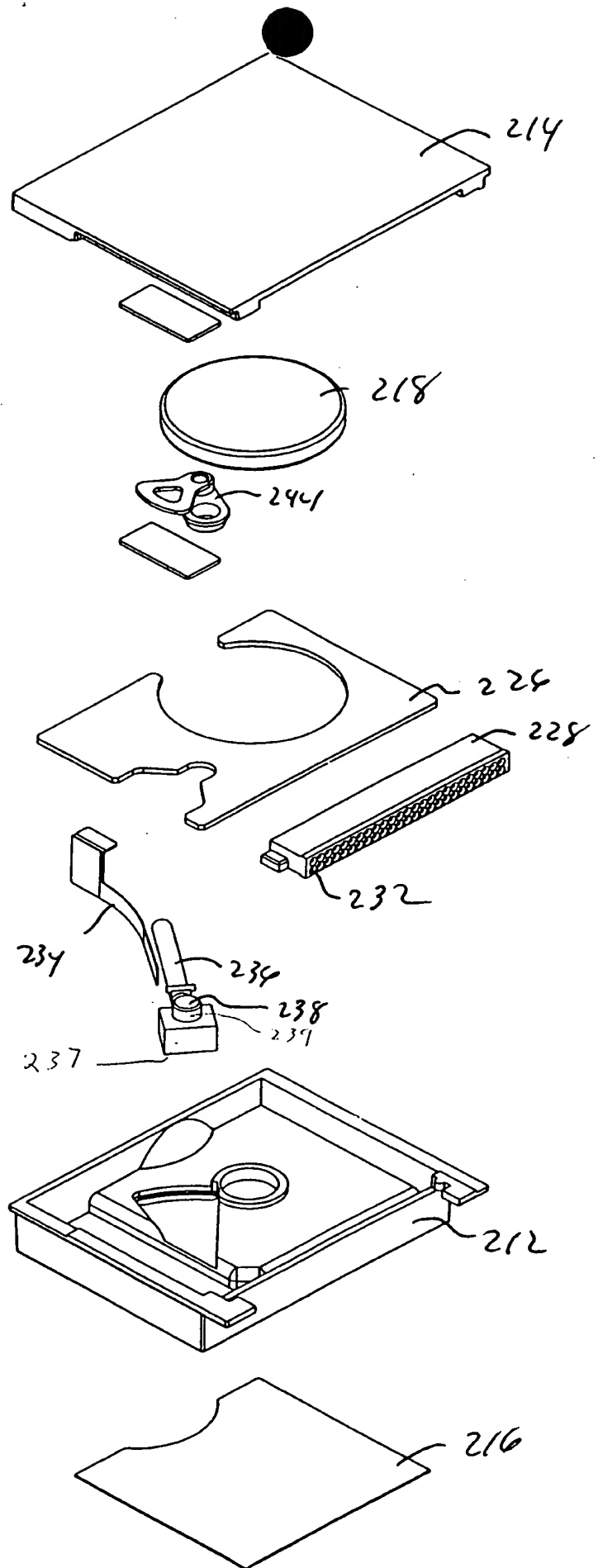
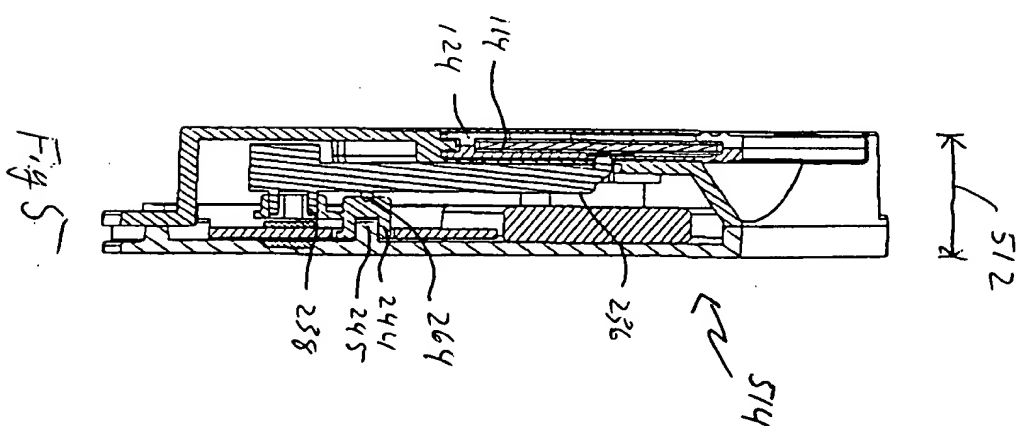
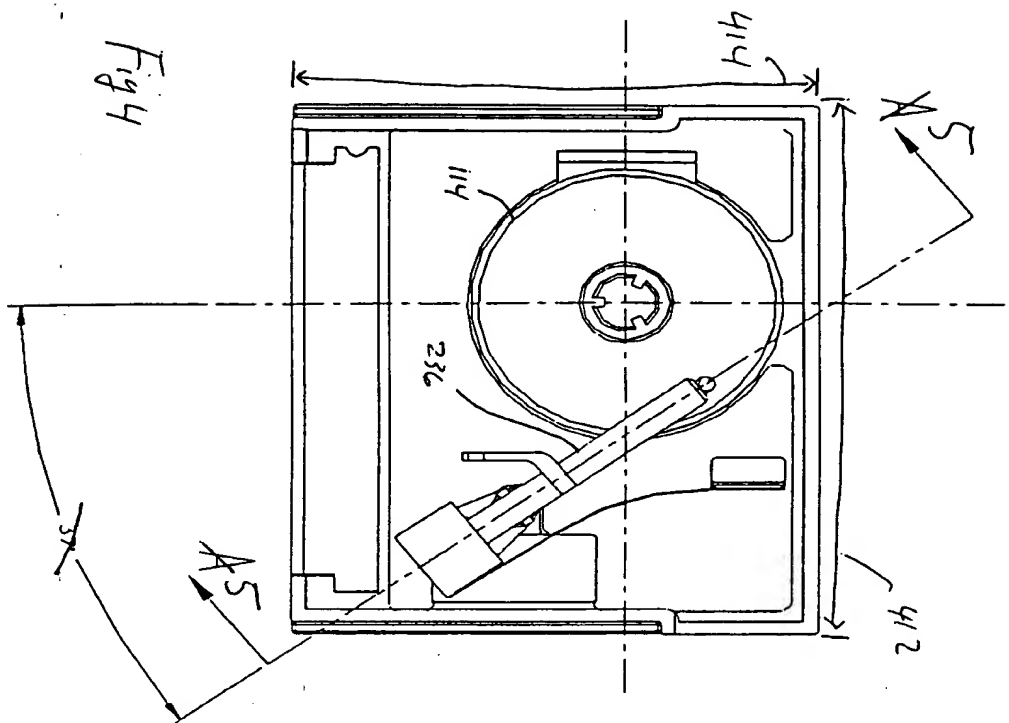
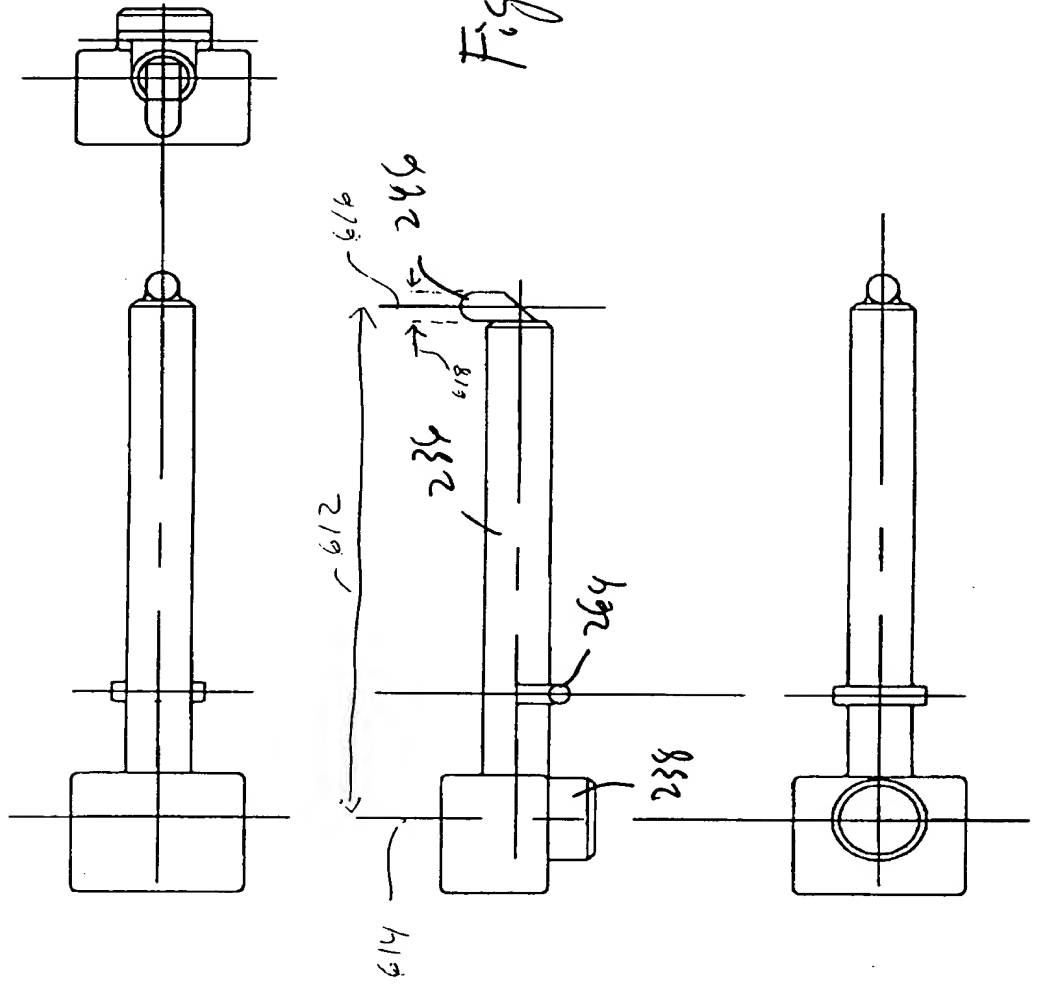


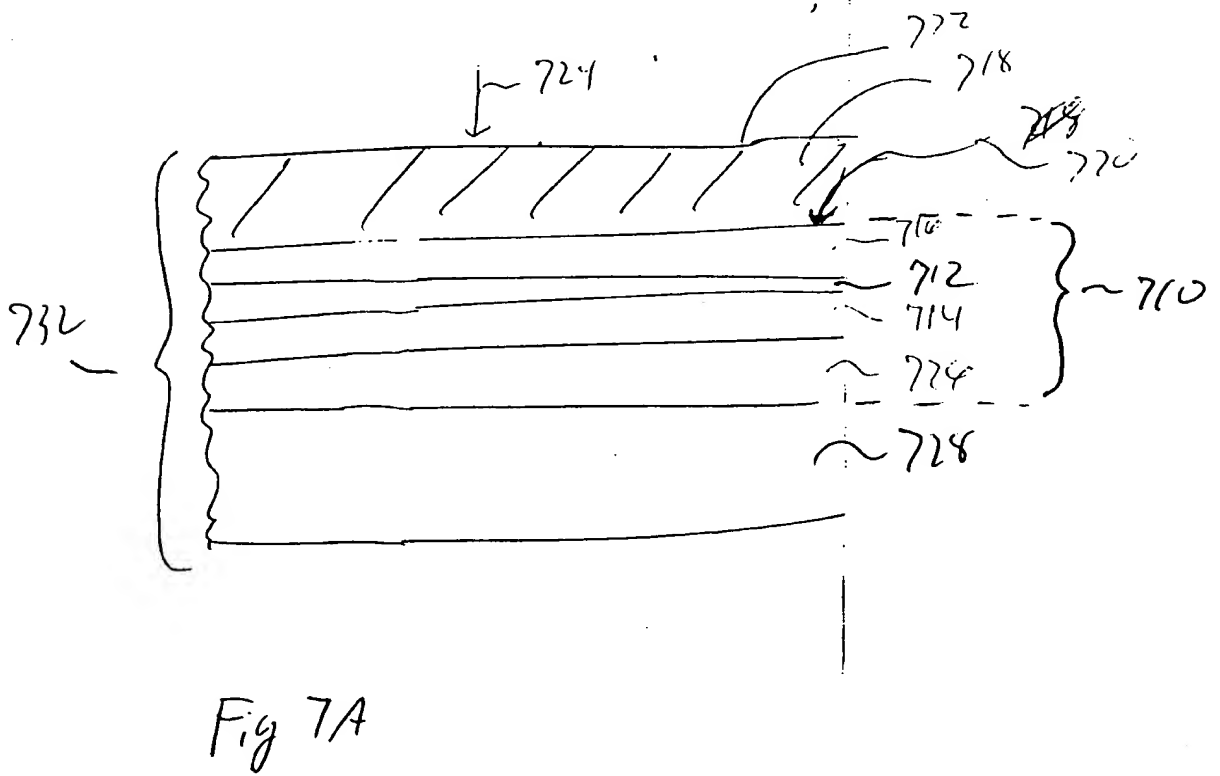
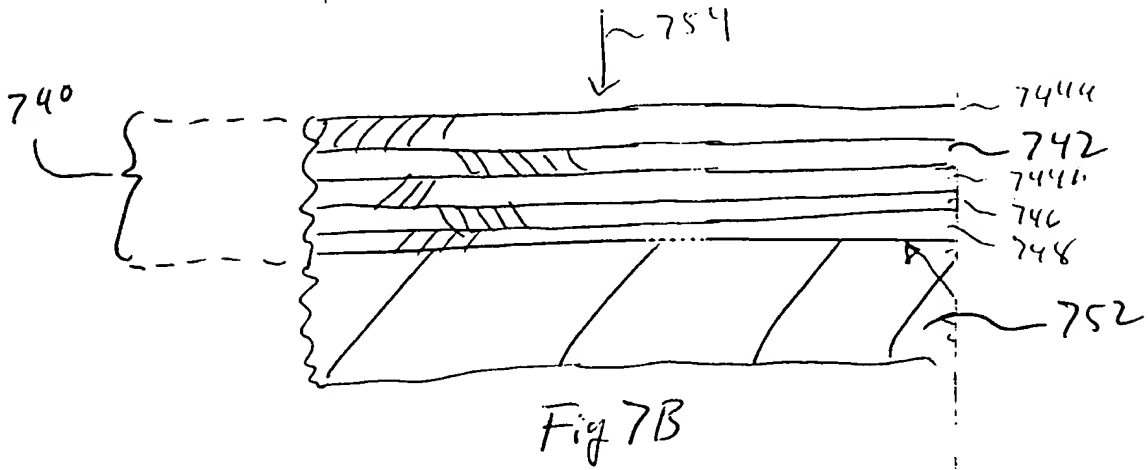
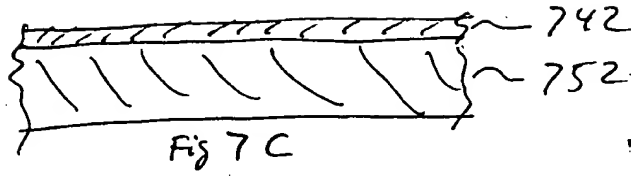
Fig 2

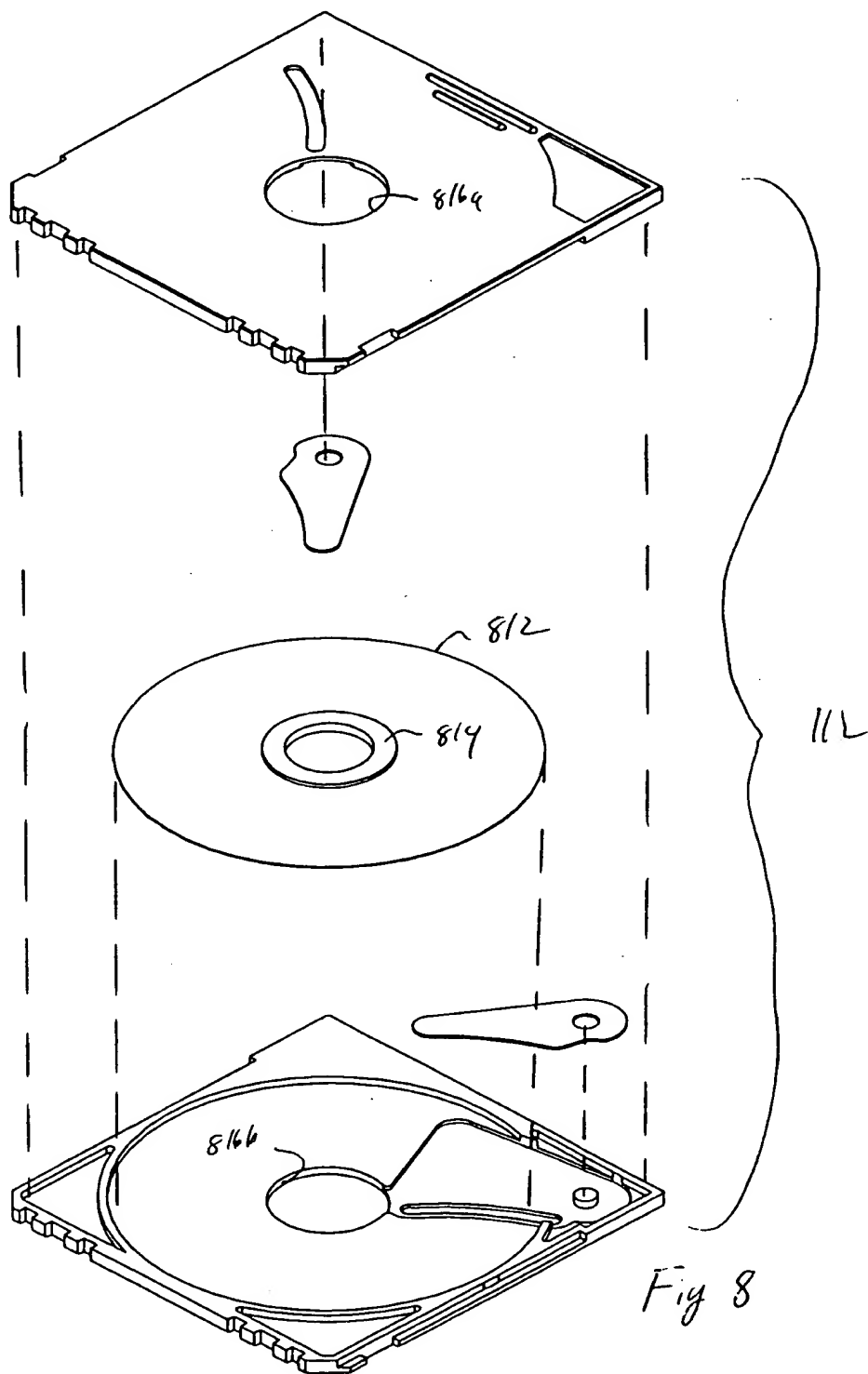
[illegible]

Fig 3









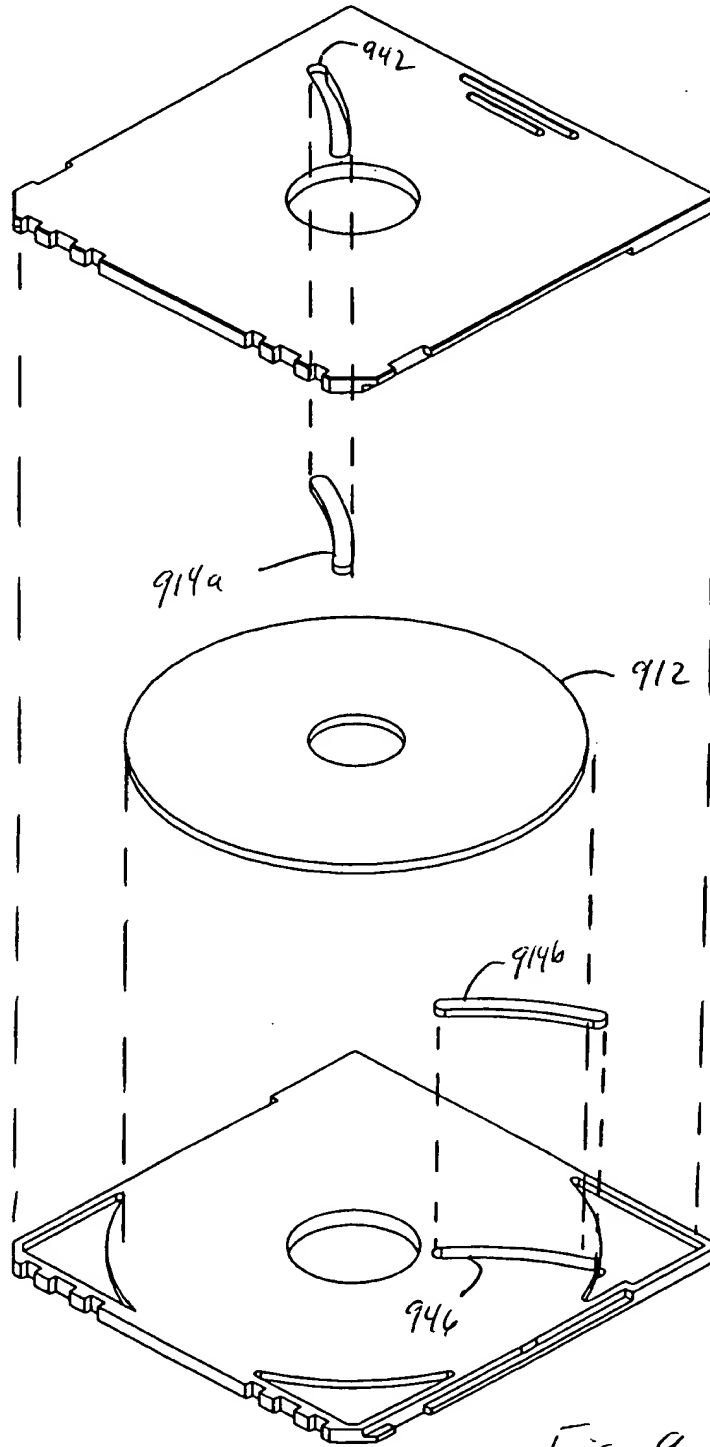


Fig 9

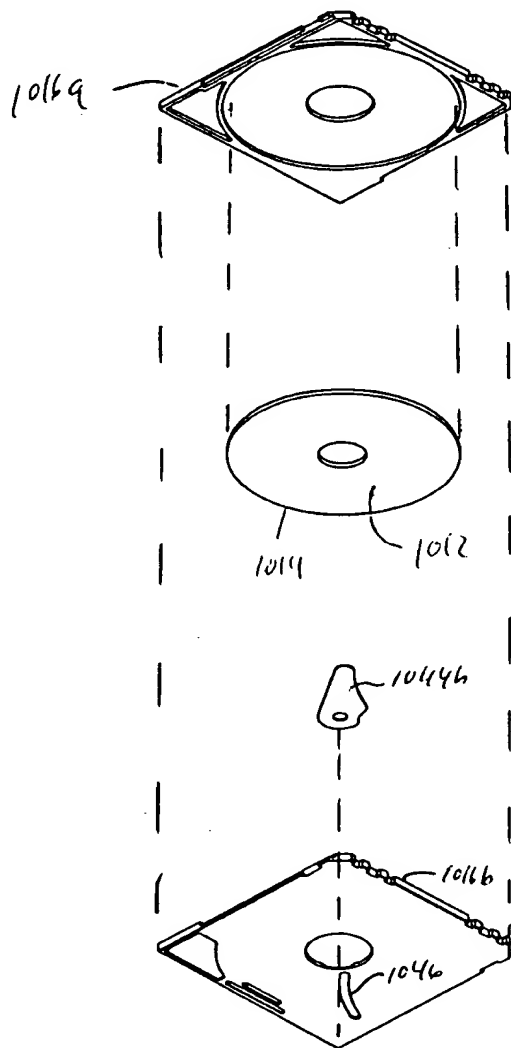


Fig 10A

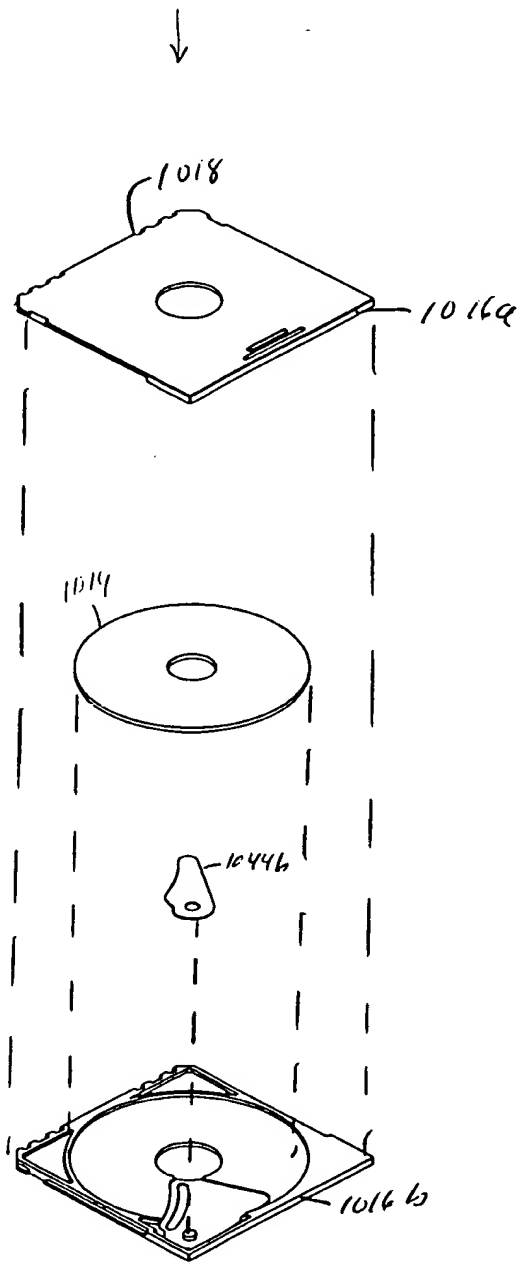


Fig 10B

Optical Head Electronics

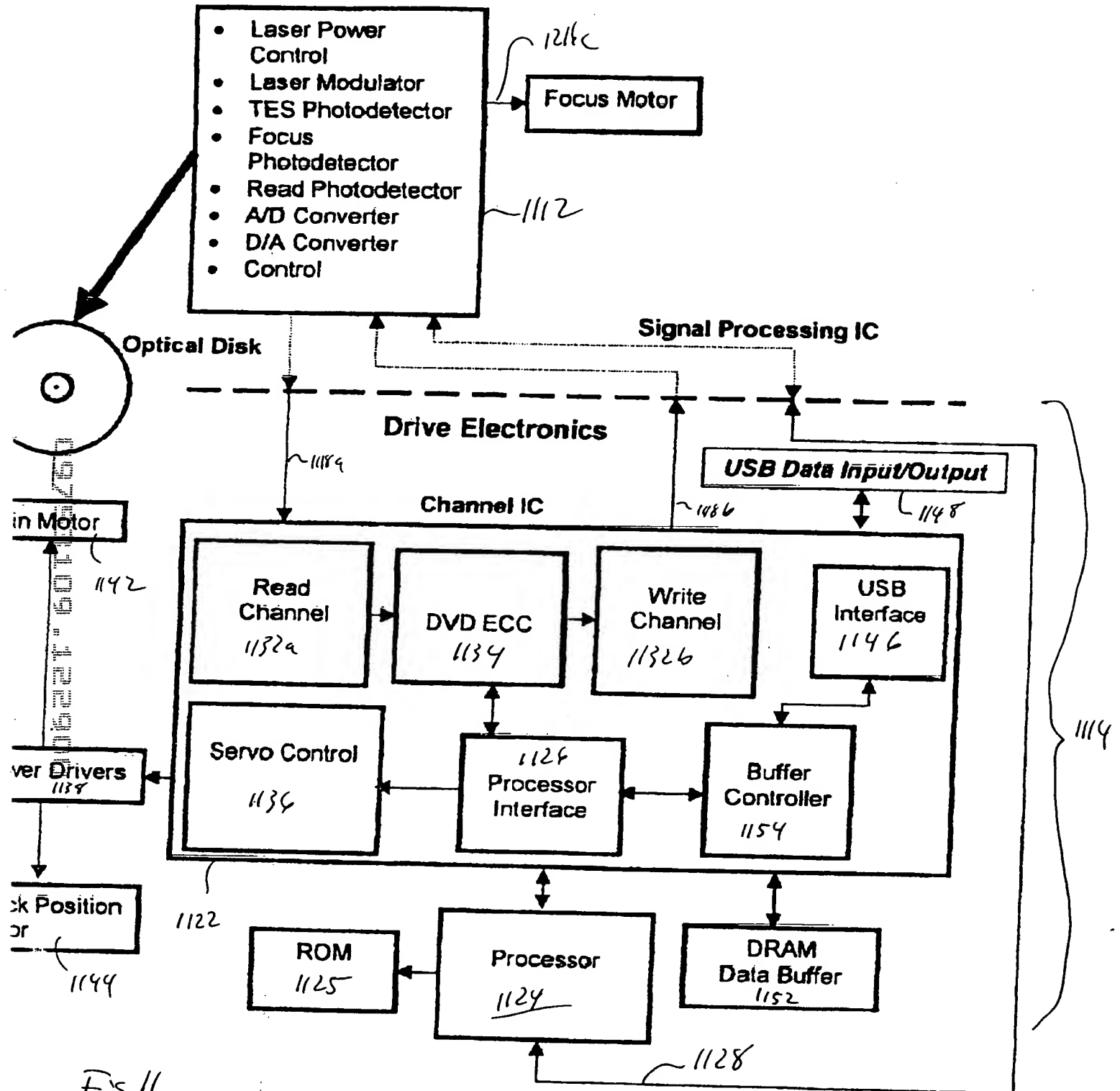
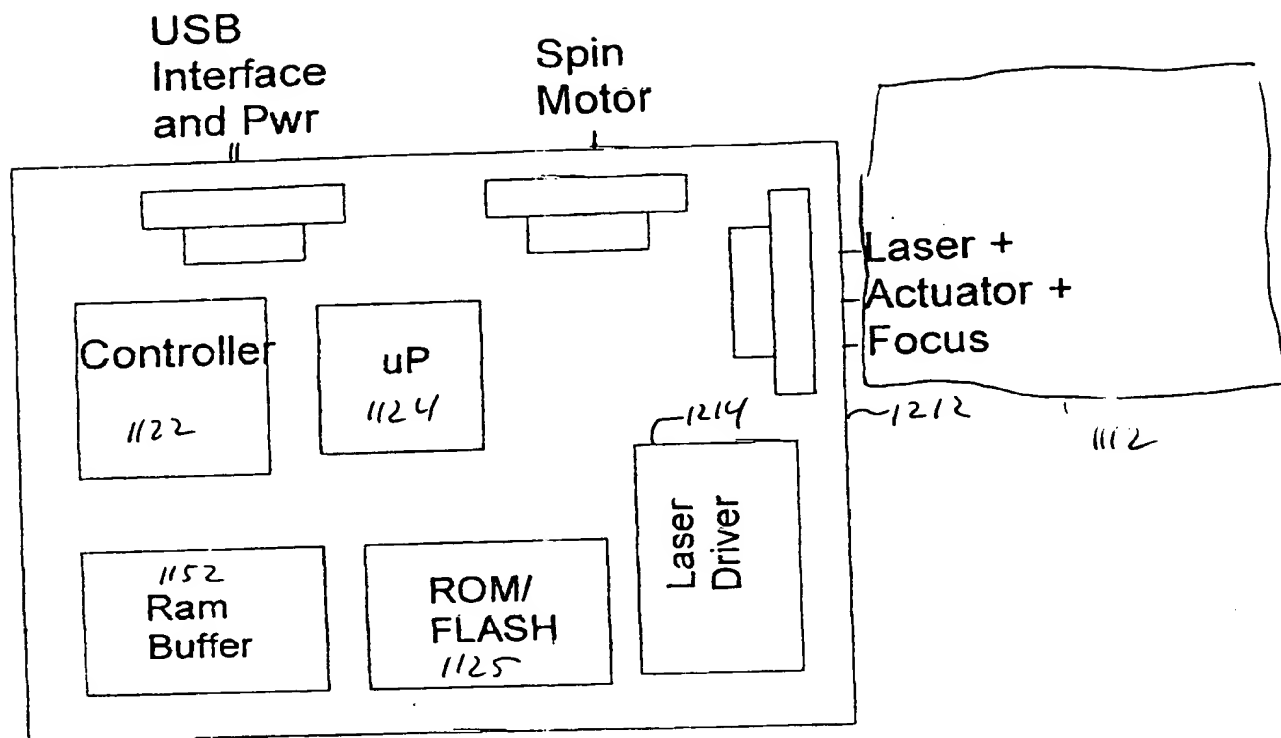


Fig 11

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DataPlay PCBA

Fig 12

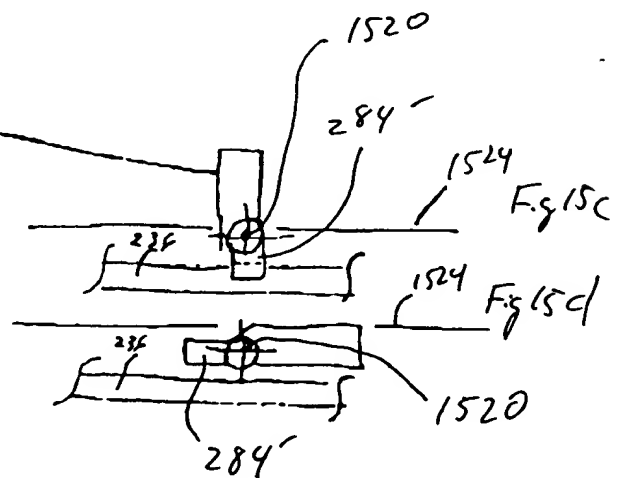
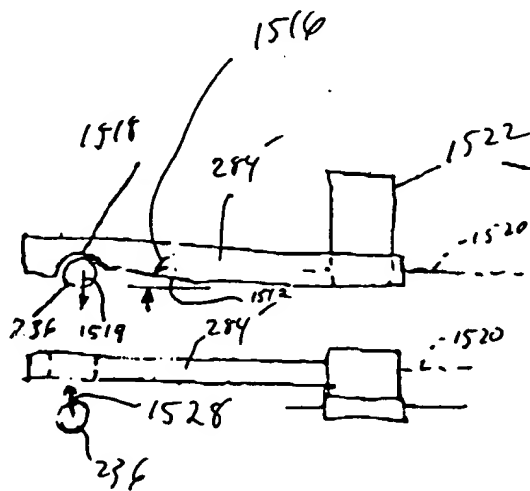
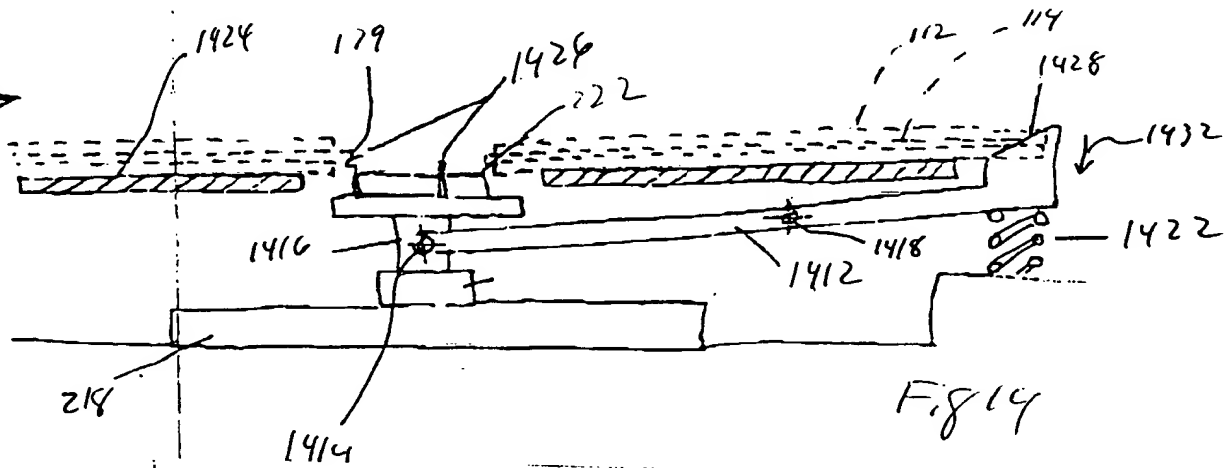
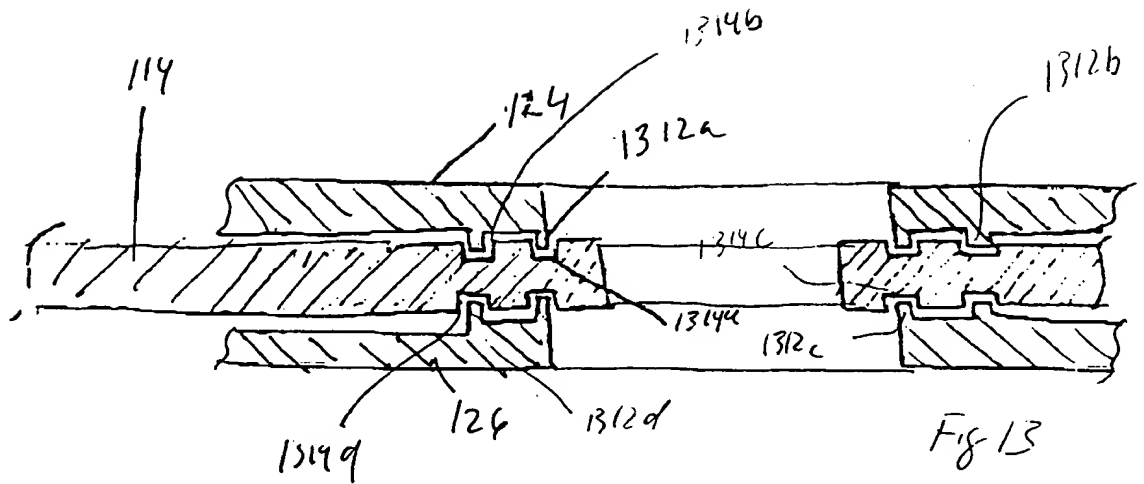


Fig 15a

Fig 15b

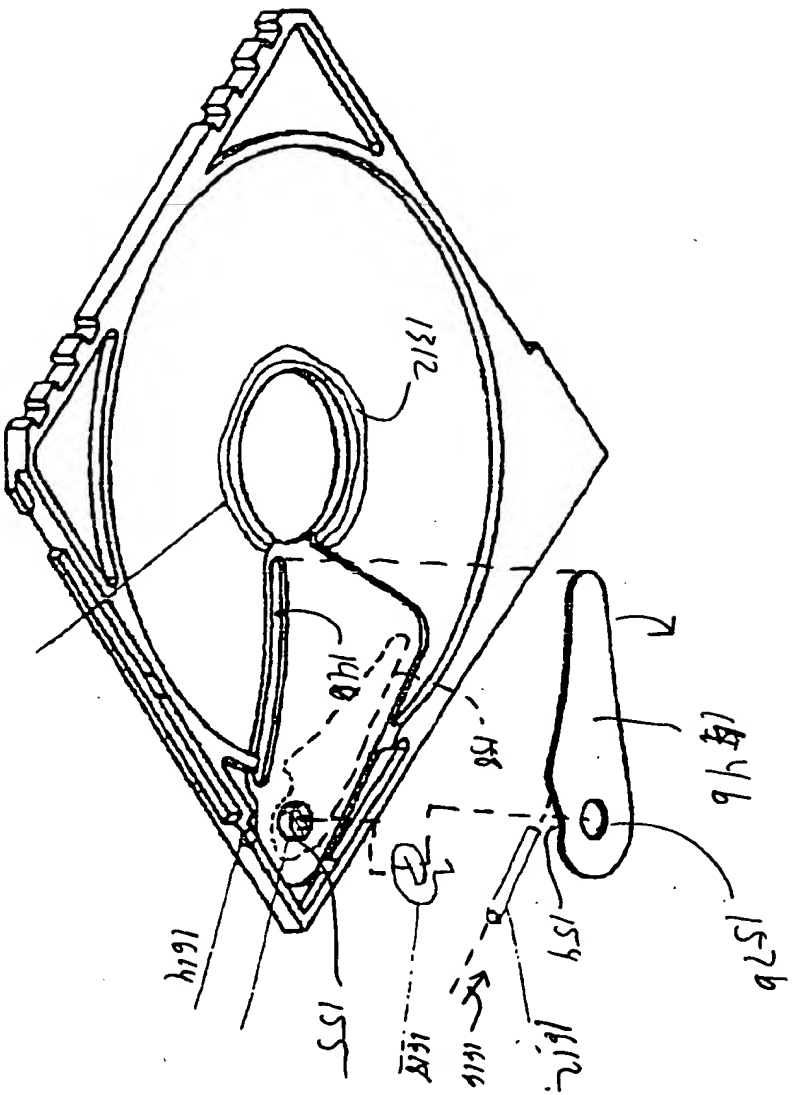


Fig 16

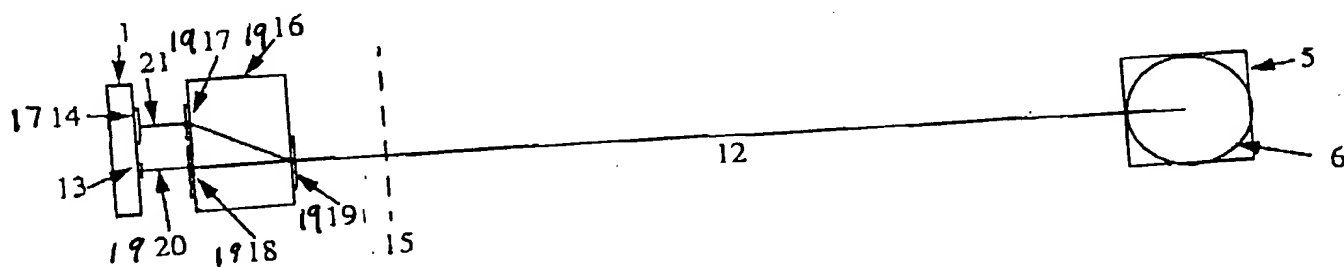
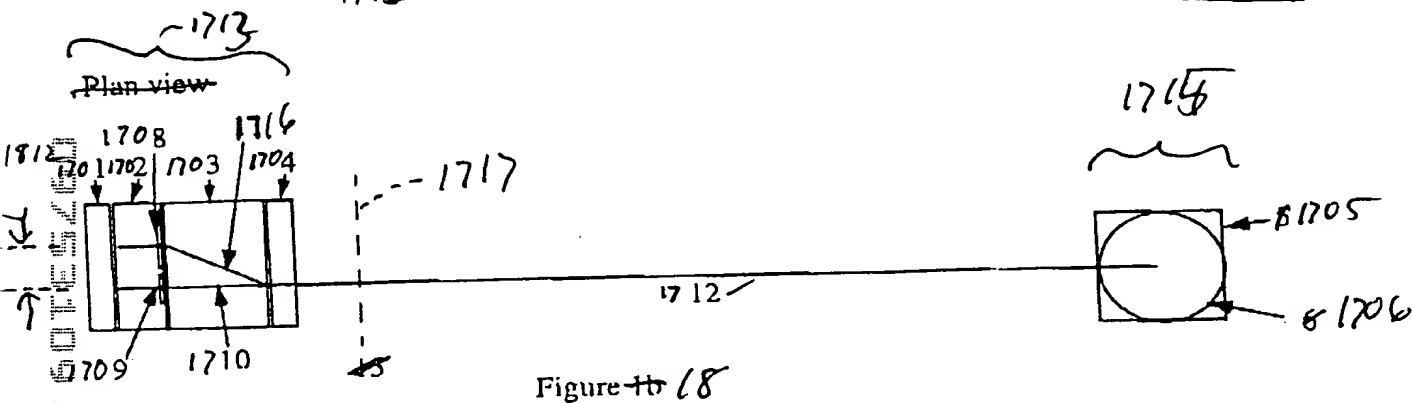
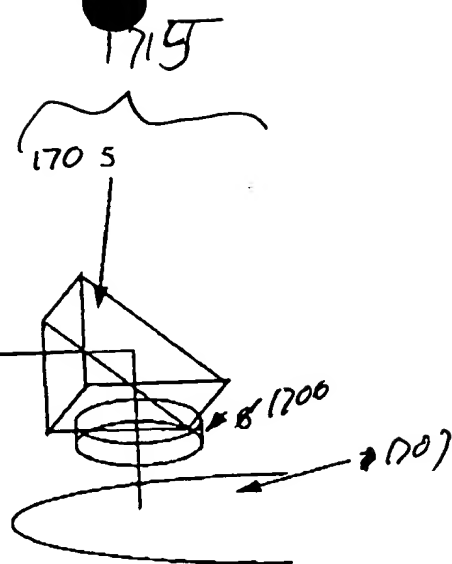
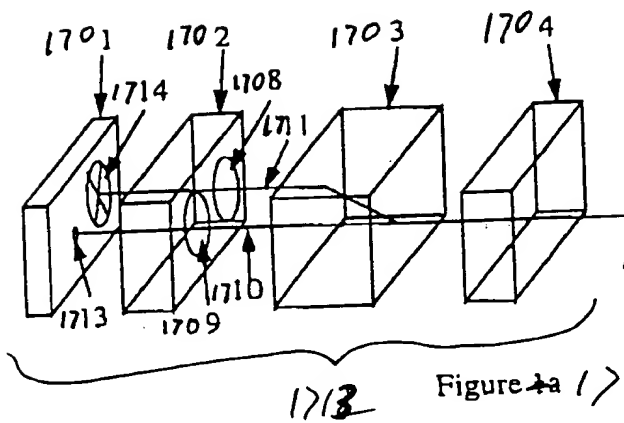
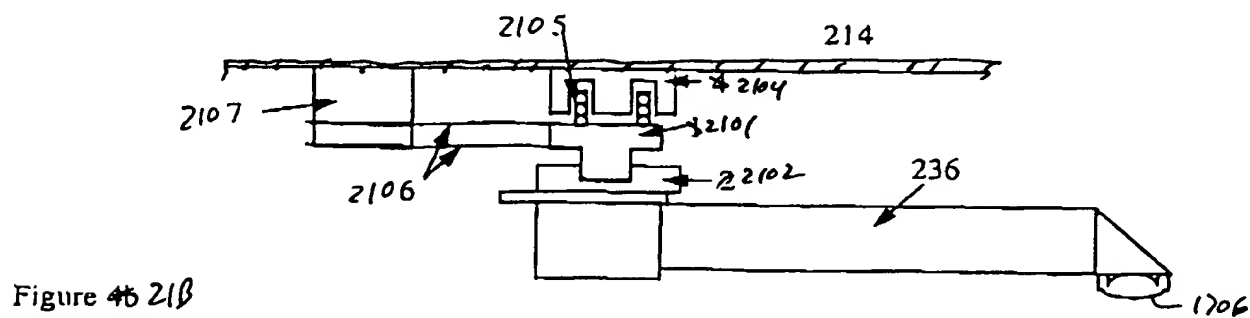
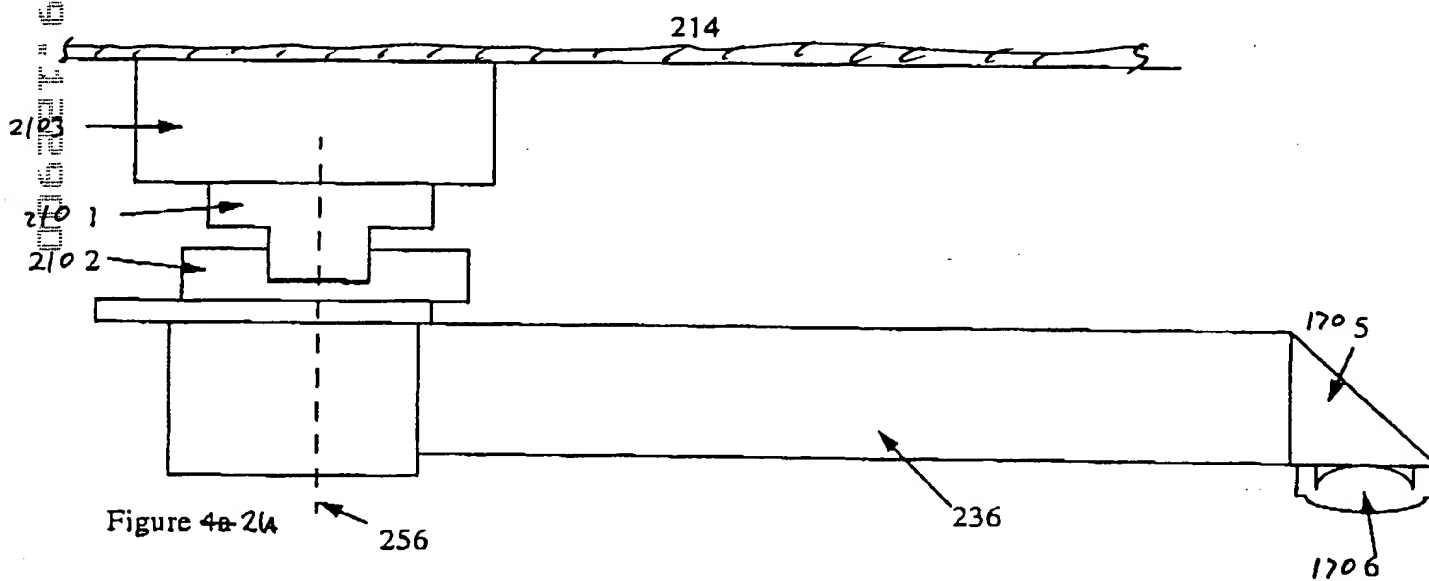
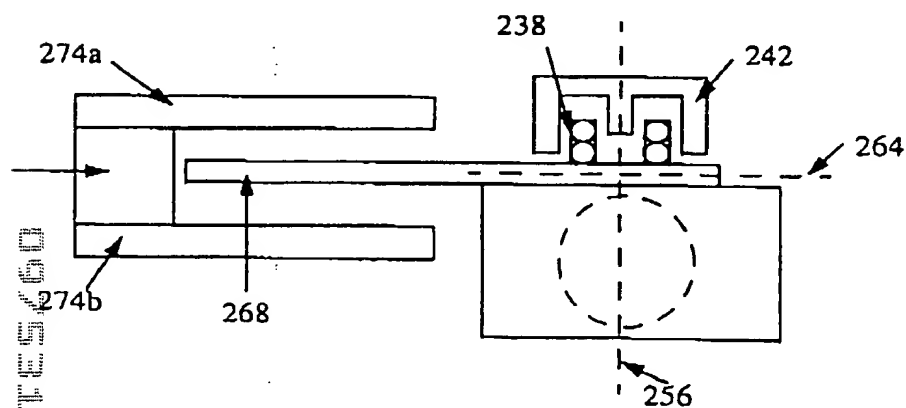
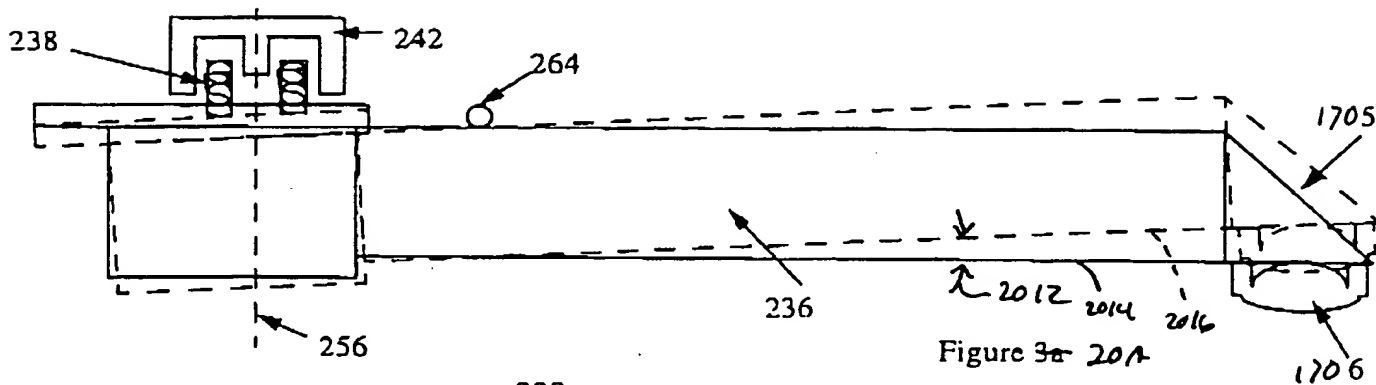
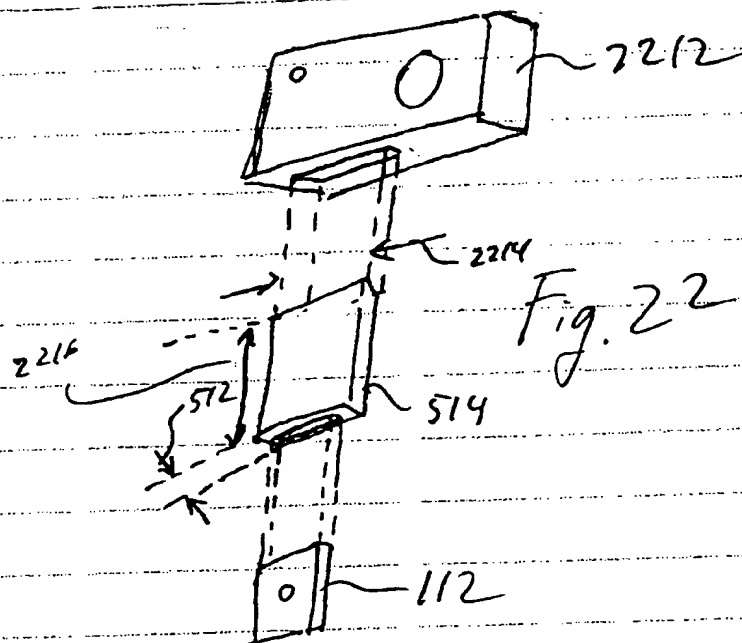
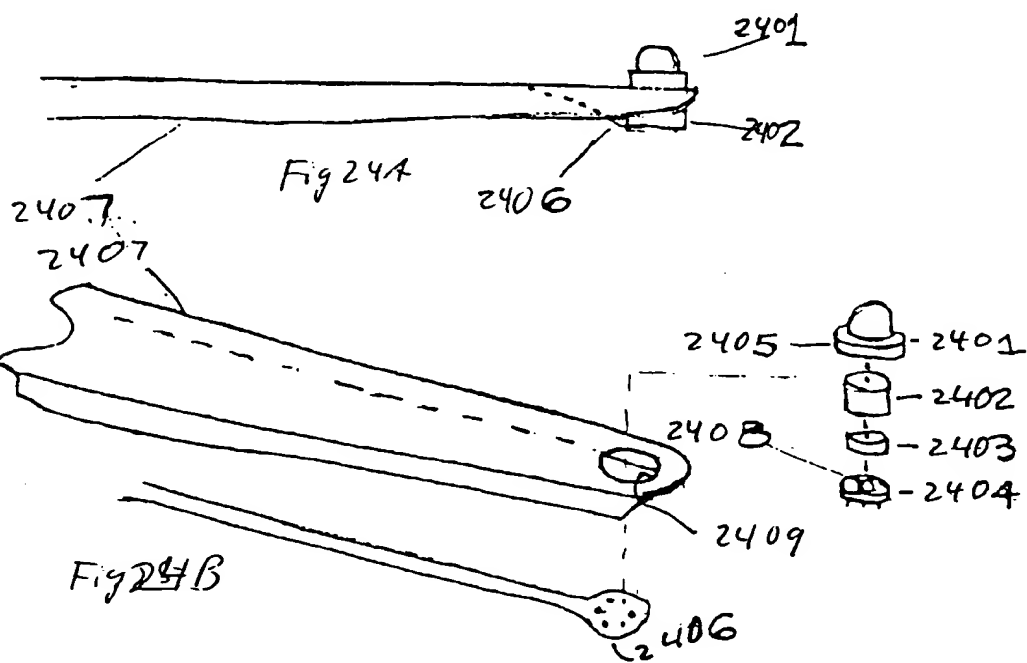
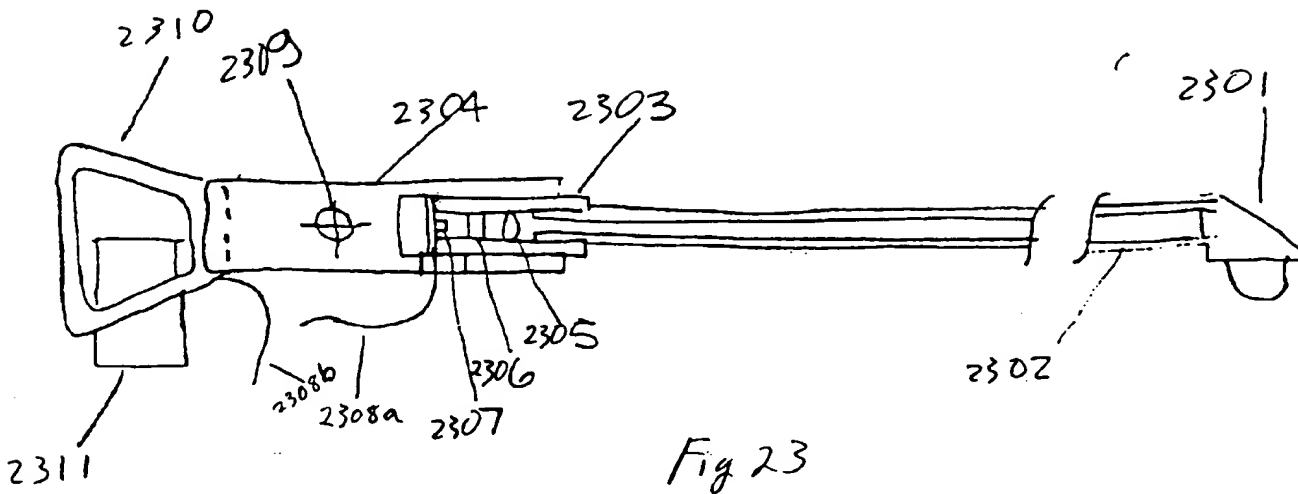


Figure 19





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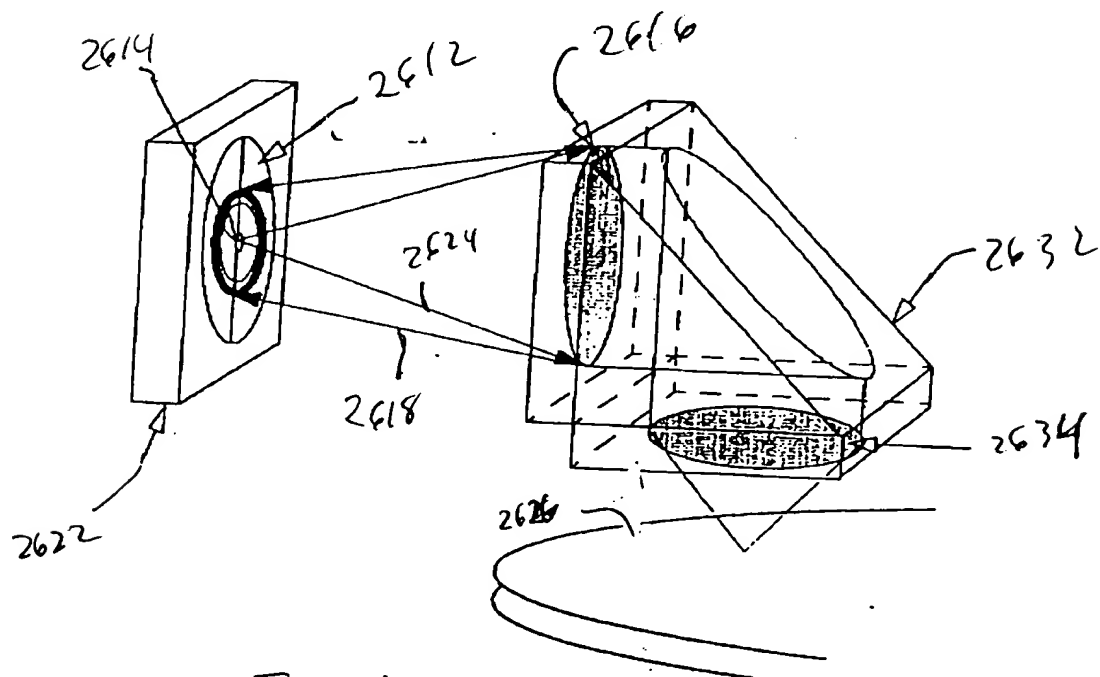
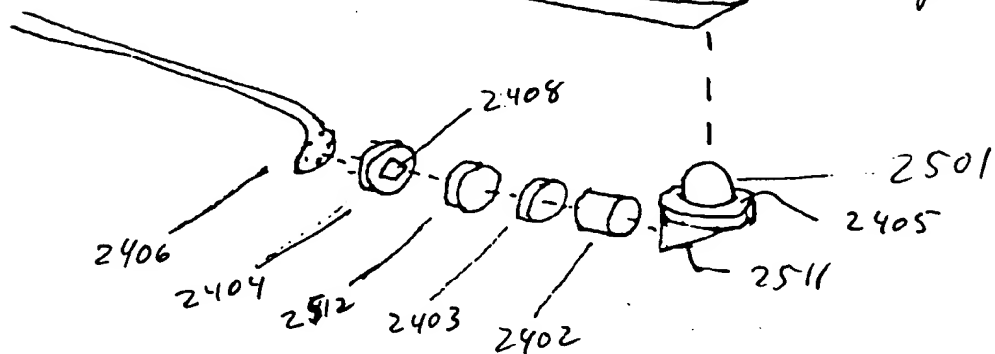
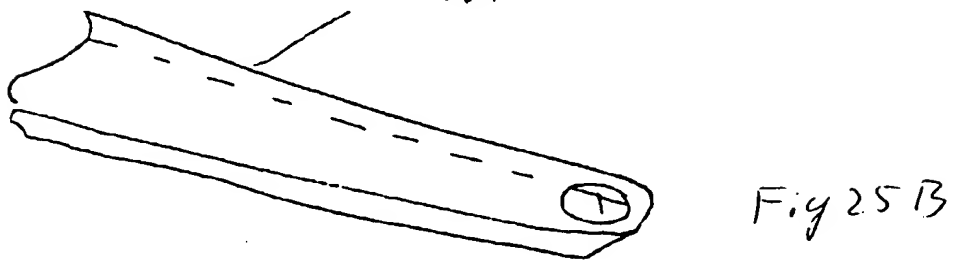
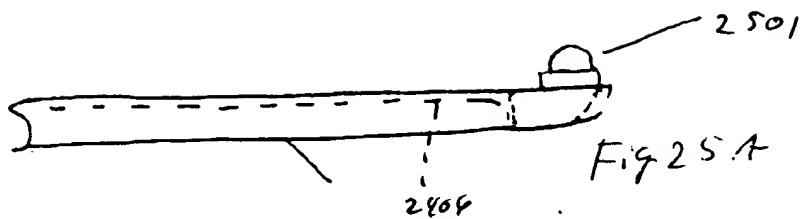


Fig 26

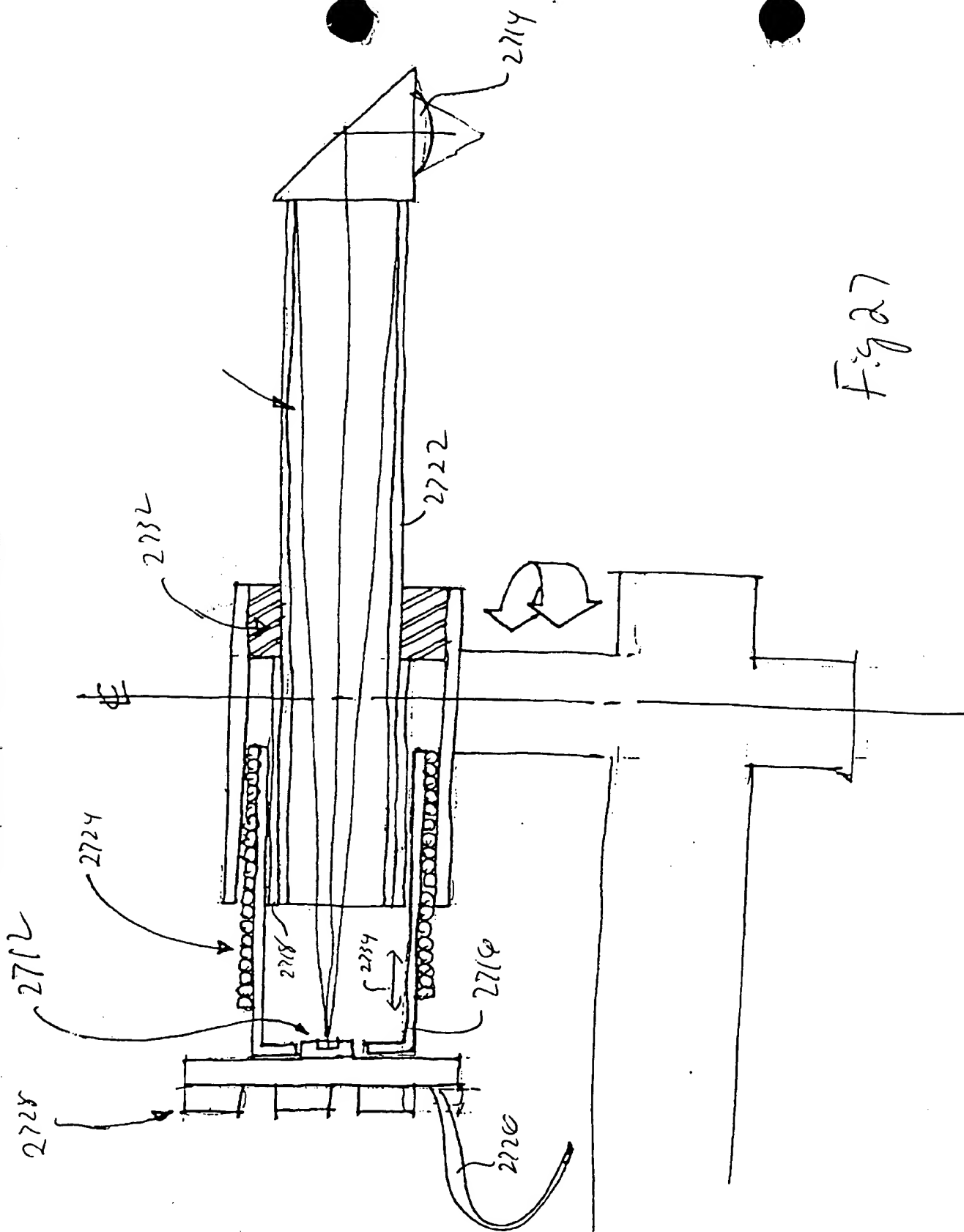


Fig 27